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Leveraging Microelectronics through Dedicated Support for Implementation in HEP Experiments

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Funding for microelectronics presents a tremendous opportunity for developing the next generation of tools enabling the cosmic frontier. The funding has focused on the very valuable ASIC (Application Specific Integrated Circuit) design and fabrication. Taking full advantage of these developments requires dedicated support for their implementation in HEP experiments, in the intersection between engineering and physics. Without this dedicated support many of these microelectronic developments could end up being not fully utilized, leading to missed opportunities for experiments and industries. To fully realize the potential of new microelectronics developments, it is essential to allocate resources towards implementing them in HEP (including device characterization and testing). Investing in a technical qualified workforce with focus on HEP to implement these devices can ensure that ASICs meet their intended specifications and function effectively in real-world applications.

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