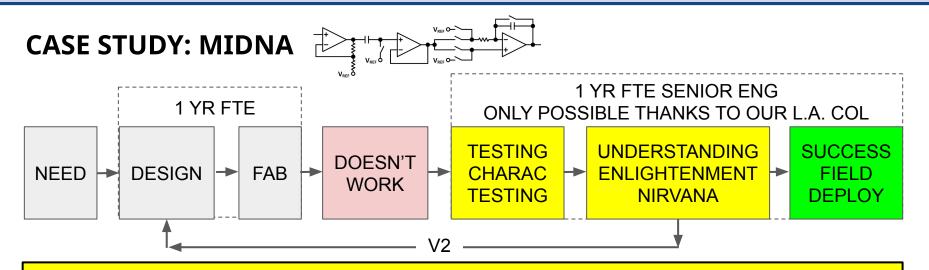
Leveraging Microelectronics through Dedicated Support for Implementation in HEP Experiments



P5 LBNL Town Hall · Feb 23, 2023 · Javier Tiffenberg



- → Compensate for fabrication uncertainties
- → Develop and implement PCB circuits for readout and control
- → Engineer and build packages for the ASICs
 - Mechanical design
 - Flex circuits or similar
 - Train technicians and build custom tools (packaging rigs, etc)
- → Link with other users and help with the deployment and tech transfer

Take home message:

NO CHARAC & TESTING = NO ASIC

- It's critical to have dedicated resources to support testing and implementation of ASICs
- People leading the implementation and testing should participate in the design process