ITkPixv1.1 – Threshold vs. BCID dependence study

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Pixel roundtable

Nov 1, 2022



Summary

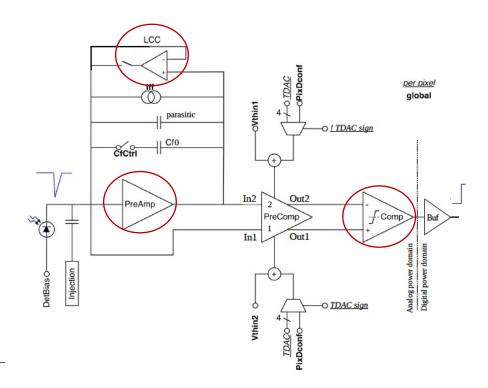
- I have studied threshold oscillation on a variety of Itkpix chips:
 - 1. v1.1 SCC with unbiased 3D sensor
 - 10 MHz dominant

- 2. v1.1 quad module with HPK planar sensor, biased at 100 V
 - 10 MHz & 40 MHz visible

(with sensor)

(without sensor)

- 3. v1.0 SCC's without sensors (single and double isolation)
 - 40 MHz dominant



- + temperature
- + single vs. double isolation

- 4. v1.1 quad module without sensors
 - 40 MHz dominant

Summary

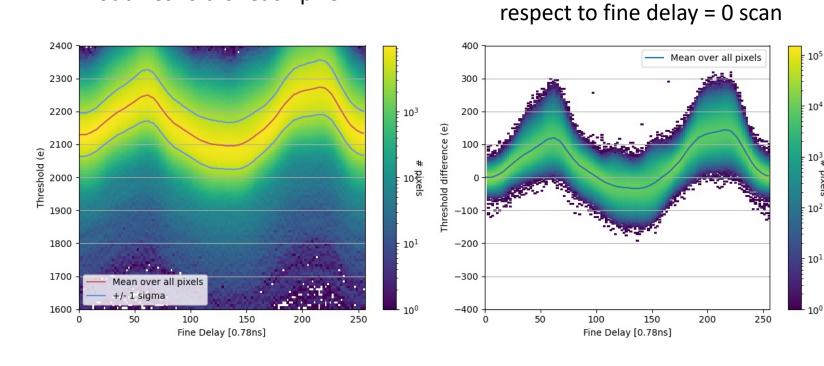
- I have studied threshold oscillation on a variety of Itkpix chips:
 - 1. v1.1 SCC with unbiased 3D sensor
 - 10 MHz dominant
 - No DiffPreamp dependence (though perhaps retuning is needed): Slide 6-7
 - No **temperature** dependence: Slide 8-9
 - No change with LCC enabled: Slide 10-11
 - 2. v1.1 guad module with HPK planar sensor, biased at 100 V
 - 10 MHz & 40 MHz visible
 - With smaller **DiffPreamp**: 40 MHz amplitude decreases, 10 MHz amplitude stays the same: Slide 14-21
 - Small changes with DiffComp: Slide 22-29

(with sensor)

(without sensor)

- 3. v1.0 SCC's without sensors (single and double isolation)
 - 40 MHz dominant
 - No significant difference between single and double isolation
 - With larger DiffComp: dispersion of oscillation increases significantly, amplitude increases slightly
- 4. v1.1 quad module without sensors
 - 40 MHz dominant

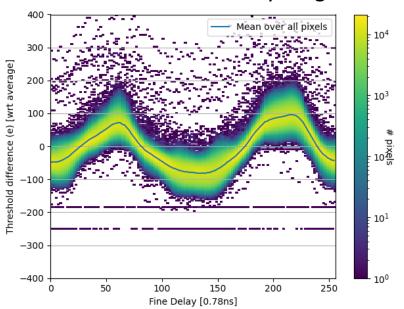
Plot threshold of each pixel:



Plot Δthr of each pixel, with

pixels

Plot Δthr of each pixel, with respect to average threshold of that pixel over entire fine delay range

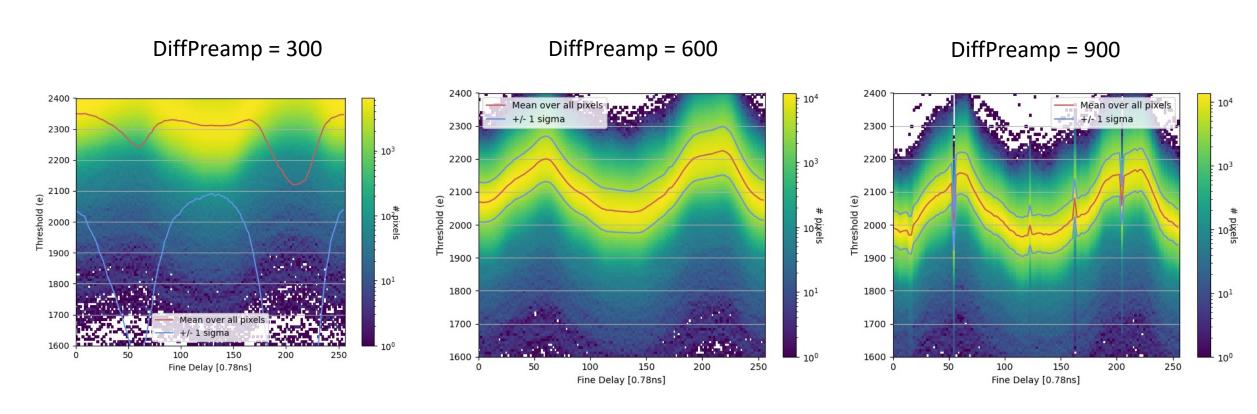


(all plots in this style in backup)

- 1. v1.1 SCC with unbiased 3D sensor
 - 10 MHz dominant
 - No DiffPreamp dependence (though perhaps retuning is needed)
 - No temperature dependence
 - No change with LCC enabled

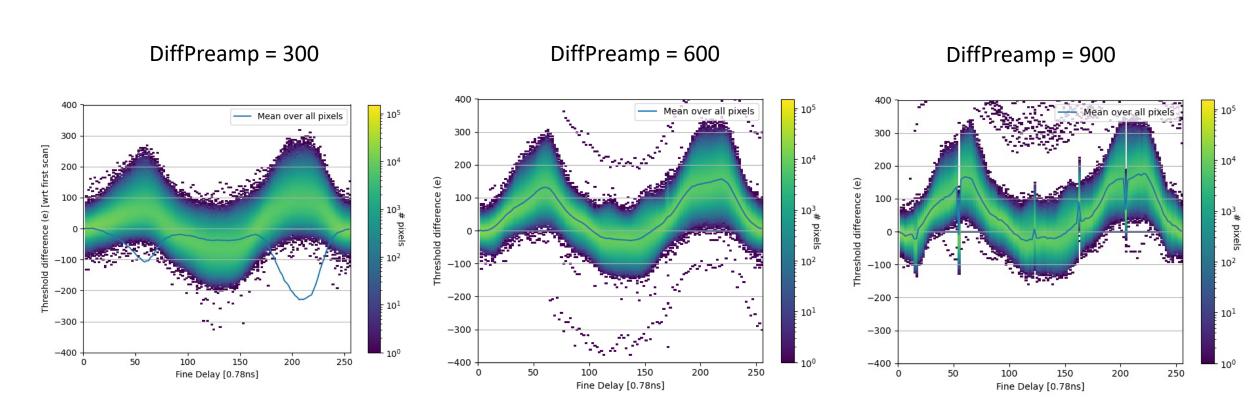
v1.1, with unbiased 3D sensor

No re-tuning in between DiffPreamp scans



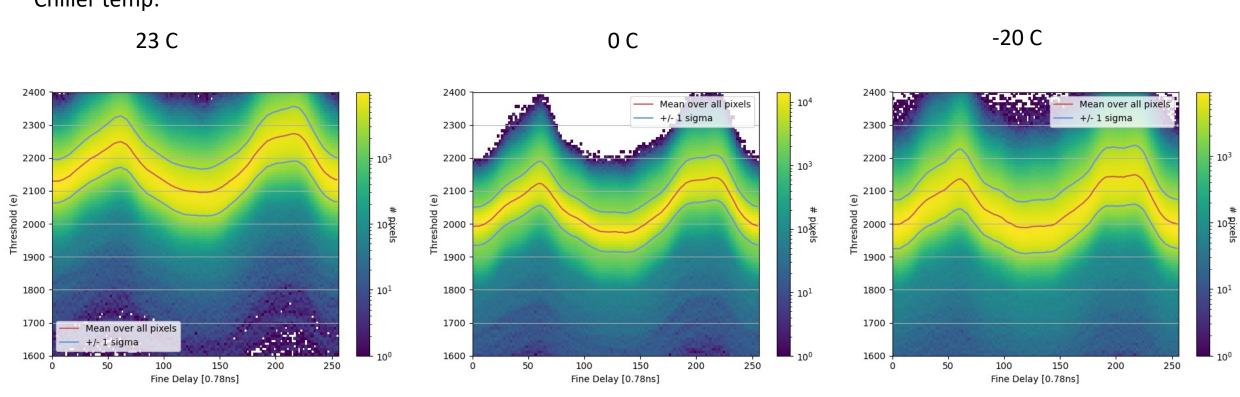
Note that there were many failed fits with this run! Because threshold increased with smaller DiffPreamp, and thr scan wasn't capturing full s-curve.

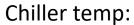
No re-tuning in between DiffPreamp scans

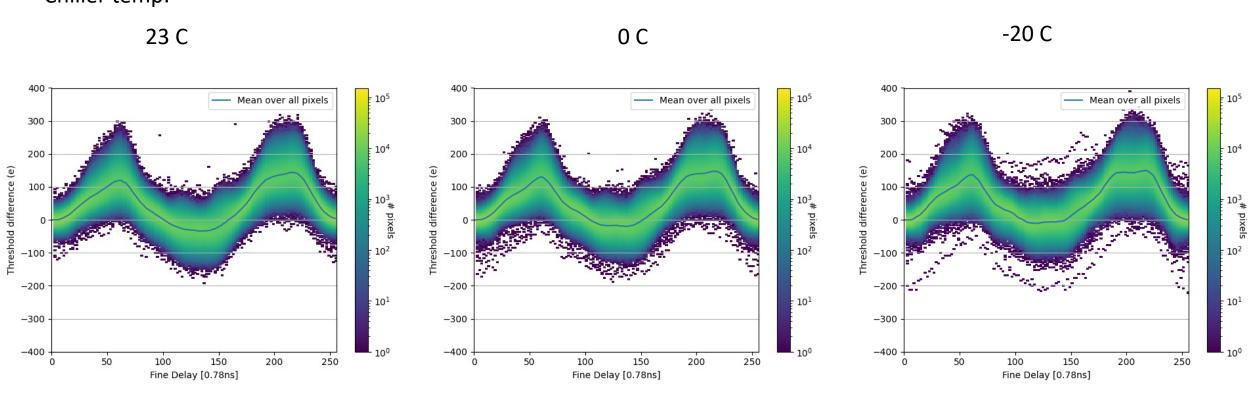


Note that there were many failed fits with this run! Because threshold increased with smaller DiffPreamp, and thr scan wasn't capturing full s-curve.

Chiller temp:



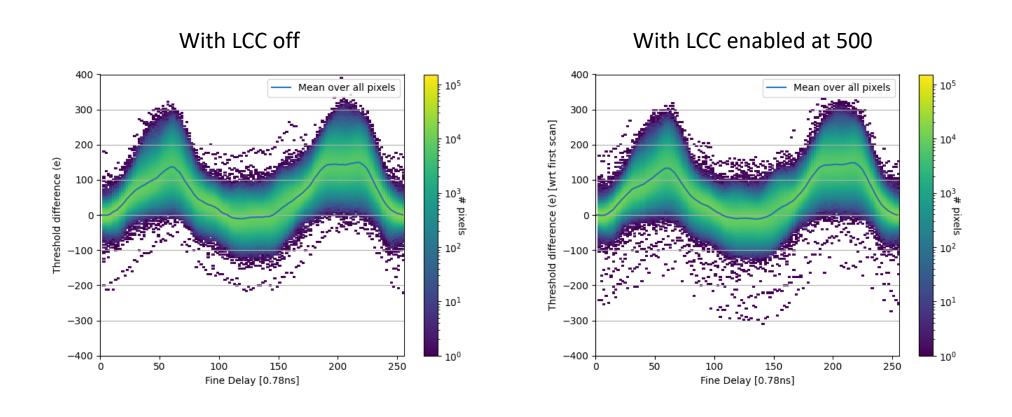




Temperature: -20C

With LCC off With LCC enabled at 500 2400 2400 +/- 1 sigma /- 1 sigma 2300 2300 - 10³ 2200 2200 - 10³ 2100 - 2000 2000 1900 2100 (e) 2000 2000 1900 10 E # pixels 1800 - 10¹ 1800 10¹ 1700 1700 1600 1600 -150 200 100 250 50 100 150 200 250 50 Fine Delay [0.78ns] Fine Delay [0.78ns]

Temperature: -20C



Temperature dependence

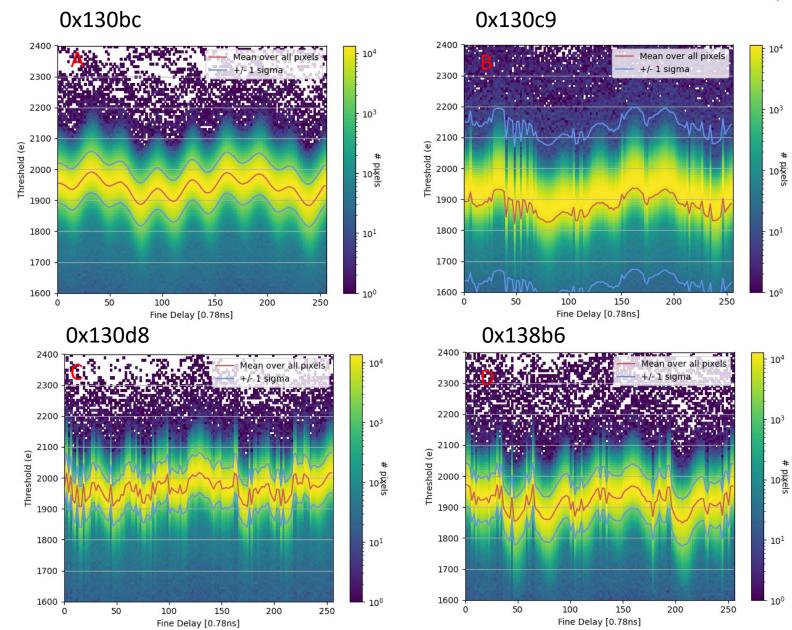
- ITkPix v1.1 quad module
- HPK planar sensor, biased @ 100V
- 15 C

v1.1, with biased HPK planar sensor

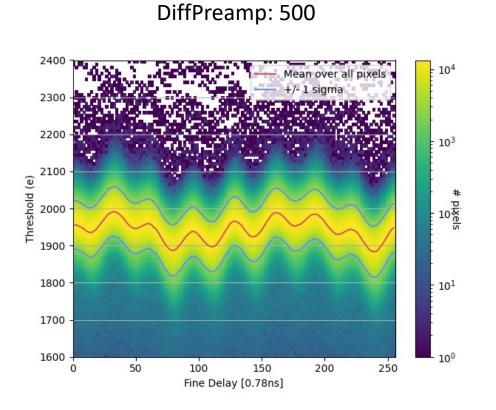
For all chips:

DiffPreamp: 500

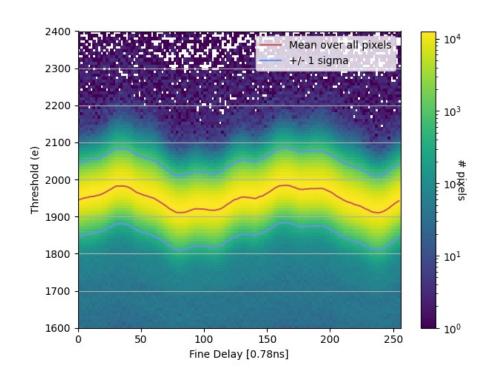
DiffComp: 500



0x130bc (chip A)



DiffPreamp: 300



(with retuning to 2000 e)

0x130bc (chip A)

DiffPreamp: 500 400 Mean over all pixels = 10⁵ 300 200 Threshold difference (e) 100 -200 -300 -400

150

Fine Delay [0.78ns]

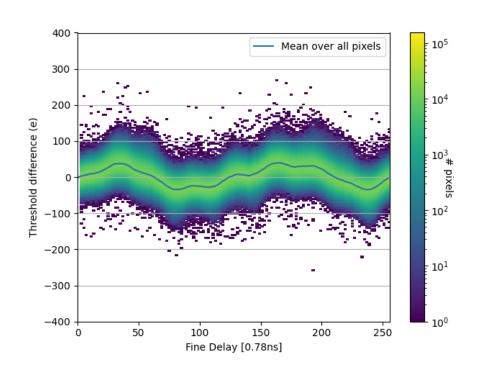
200

50

100

250

DiffPreamp: 300

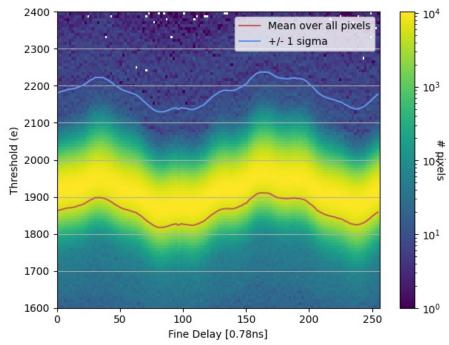


(with retuning to 2000 e)

0x130c9 (chip B)

DiffPreamp: 500 104 Mean over all pixels 2300 2200 - 10³ 2100 2000 1900 1900 # pixels 1900 1800 10¹ 1700 1600 250 100 150 200 Fine Delay [0.78ns]

DiffPreamp: 300



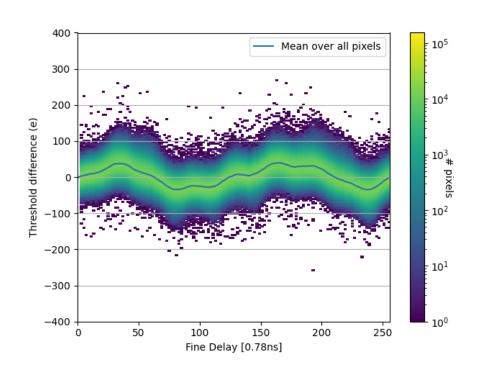
(with retuning to 2000 e)

0x130c9 (chip B)

DiffPreamp: 500 400 Mean over all pixels = 10⁵ 300 200 Threshold difference (e) 100 -200 -300 -400 250 50 100 150 200

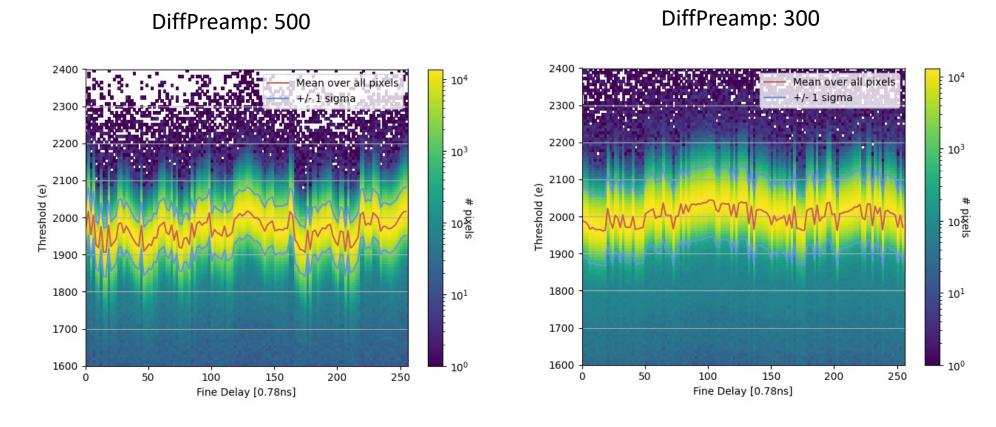
Fine Delay [0.78ns]

DiffPreamp: 300



(with retuning to 2000 e)

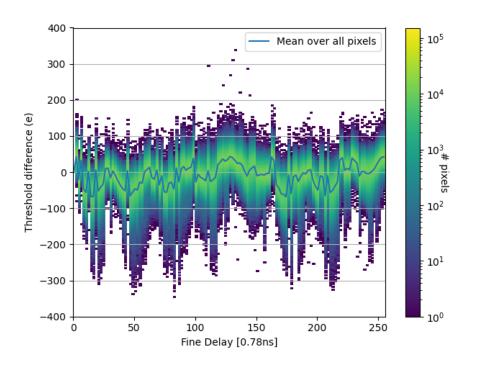
0x130d8 (chip C)



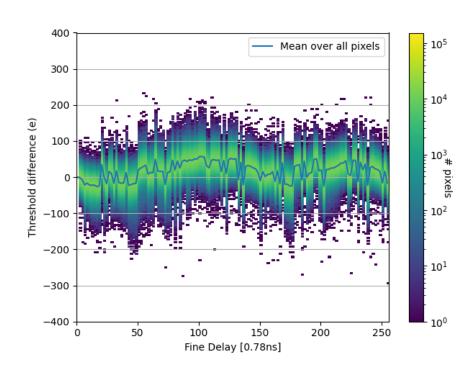
(with retuning to 2000 e)

0x130d8 (chip C)

DiffPreamp: 500



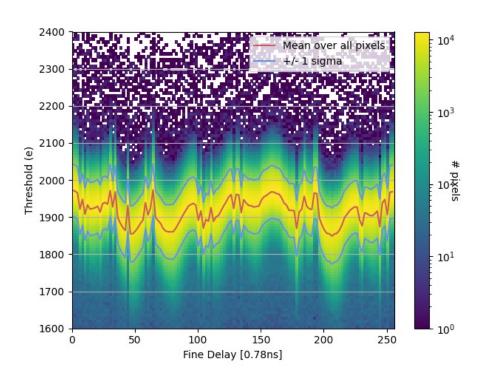
DiffPreamp: 300



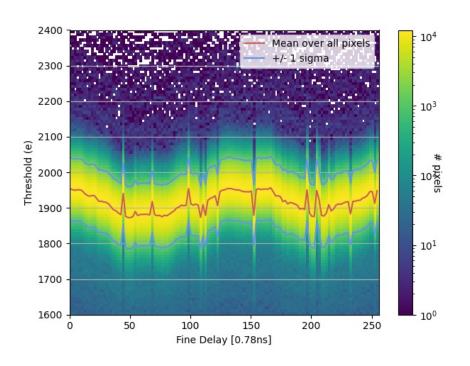
(with retuning to 2000 e)

0x138b6 (chip D)

DiffPreamp: 500



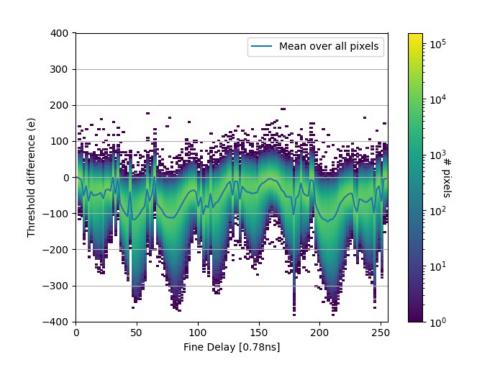
DiffPreamp: 300



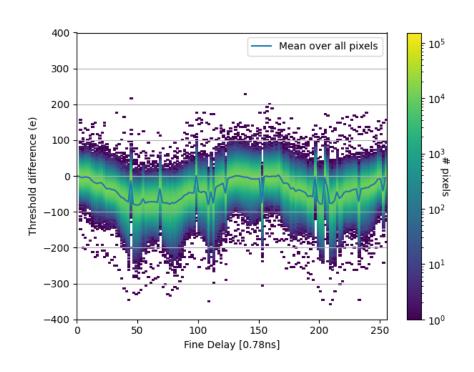
(with retuning to 2000 e)

0x138b6 (chip D)

DiffPreamp: 500

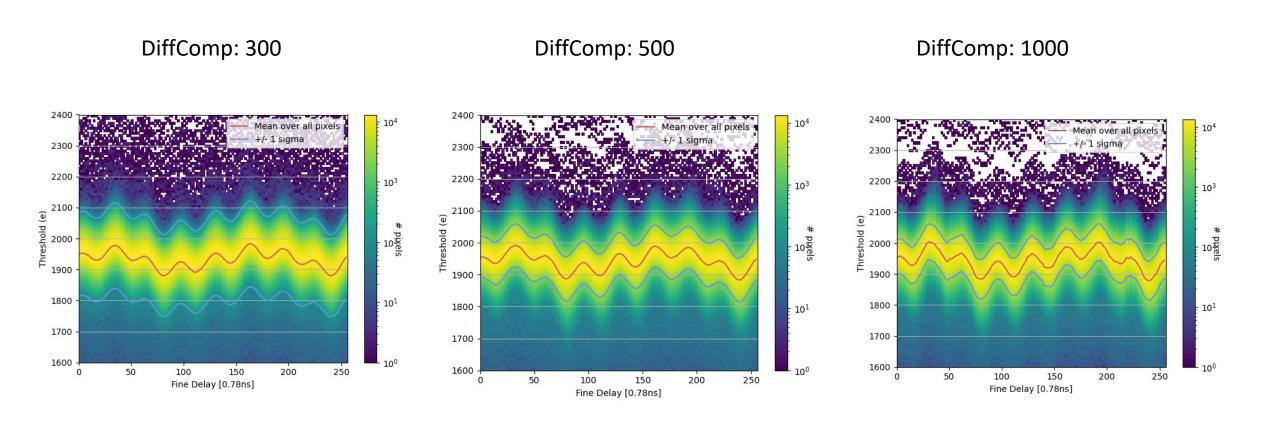


DiffPreamp: 300

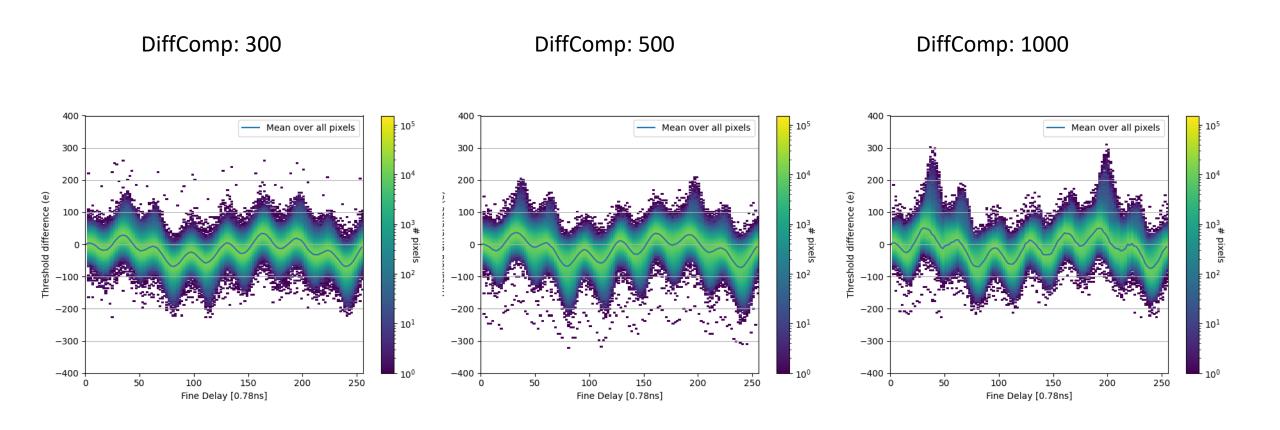


(with retuning to 2000 e)

0x130bc (chip A)

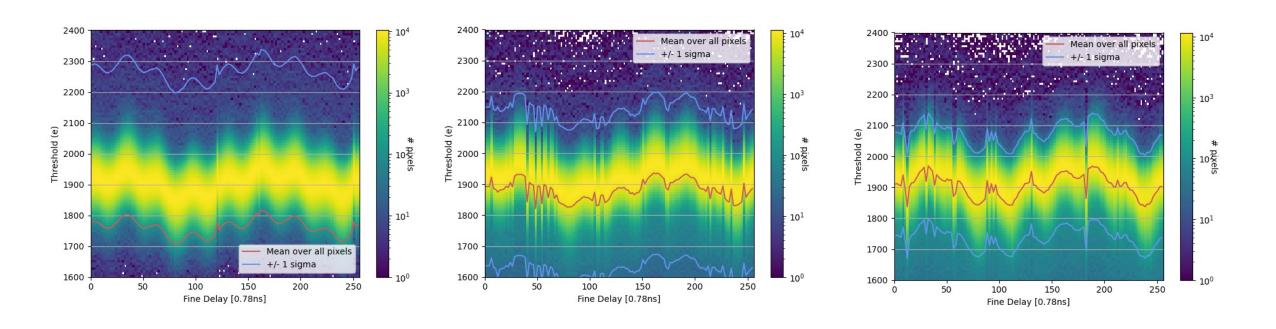


0x130bc (chip A)

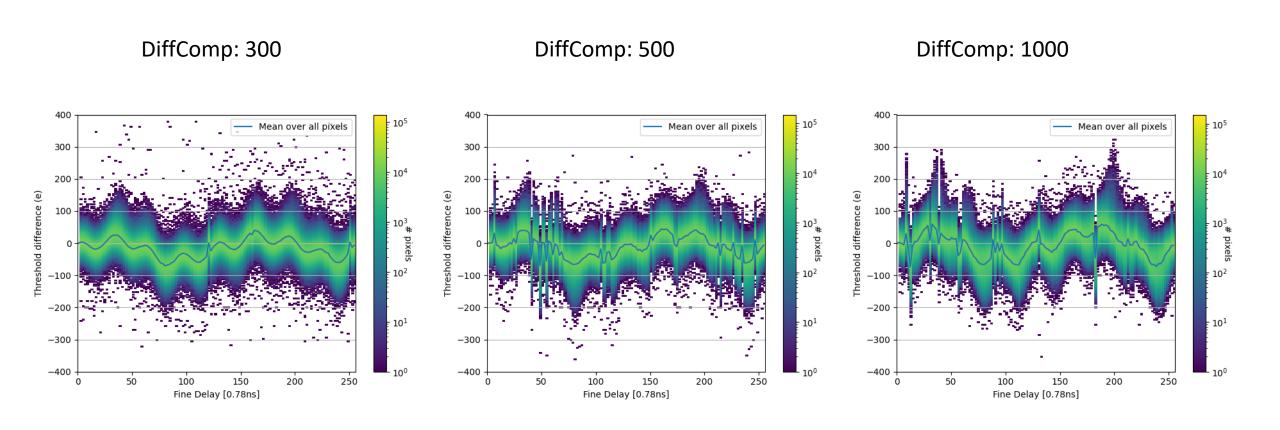


0x130c9 (chip B)

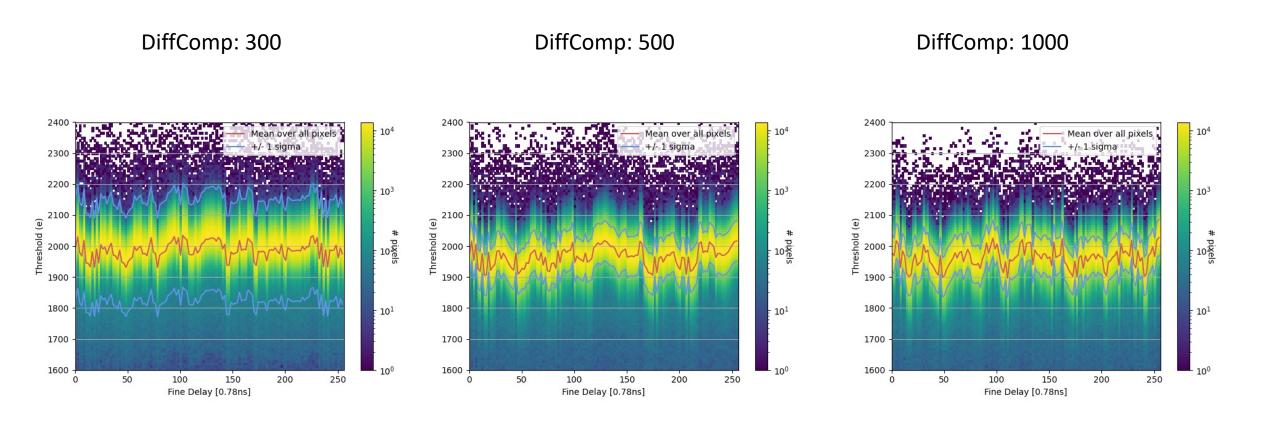




0x130c9 (chip B)



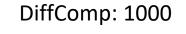
0x130d8 (chip C)

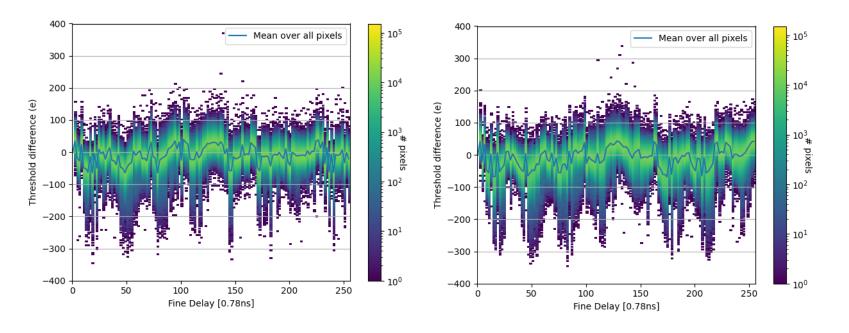


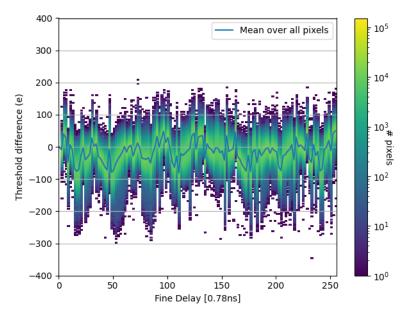
0x130d8 (chip C)

DiffComp: 300

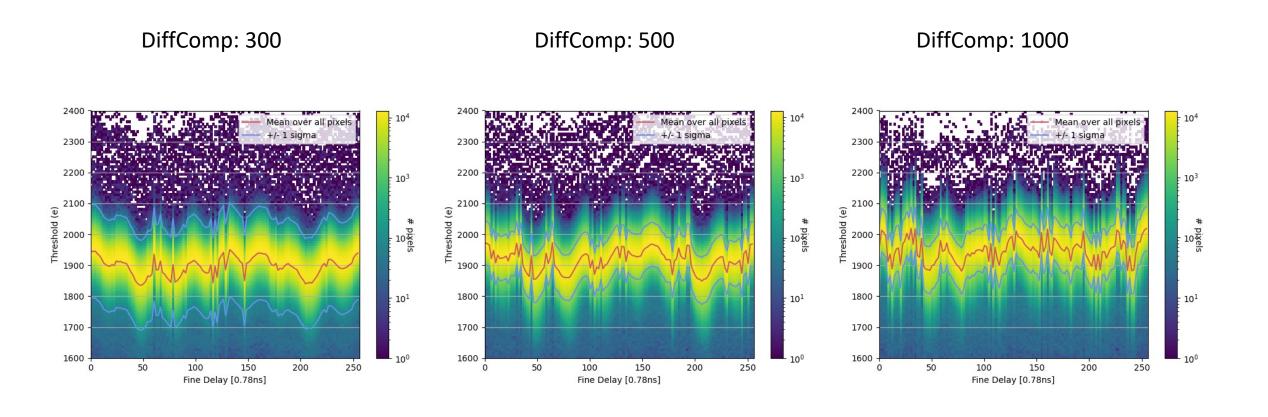
DiffComp: 500



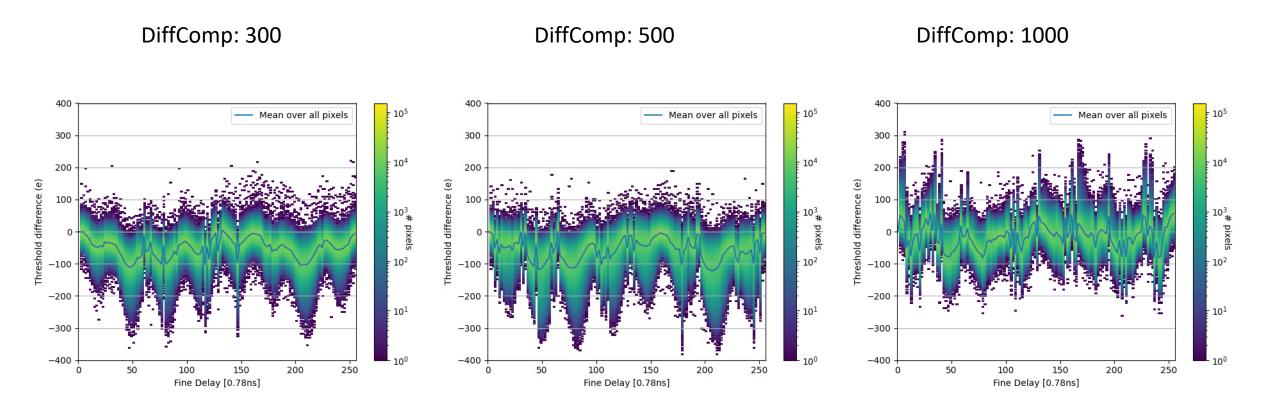




0x138b6 (chip D)

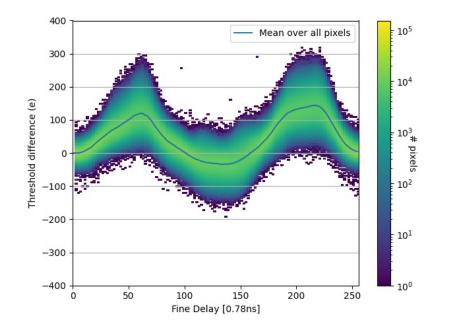


0x138b6 (chip D)

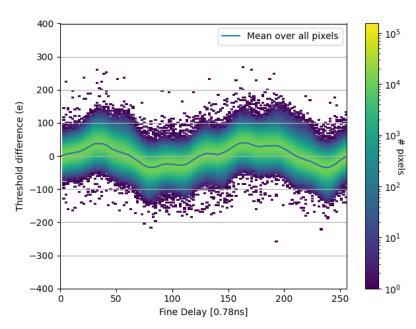


Focus of next studies

3D sensor, default config, room temp



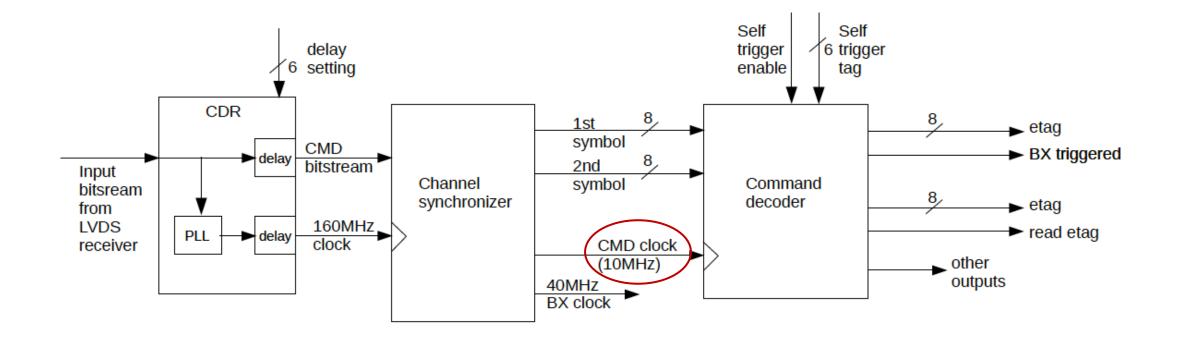
HPK planar sensor, DiffPreamp = 300



- Amplitude of 10 MHz oscillation is smaller in HPK planar sensor why?
- 2. 40 MHz not clearly visible in 3D sensor why?
- LCC is different, but no change when turning on LCC in 3D sensor
- Differences in electrical connections between quad and SCC
- Powering is different (LDO in SCC vs. SLDO in quad)

Next steps:

- Test two more SCC's with
 3D sensor
- 2. Power SCC's in SLDO
- 3. Power PLL separately

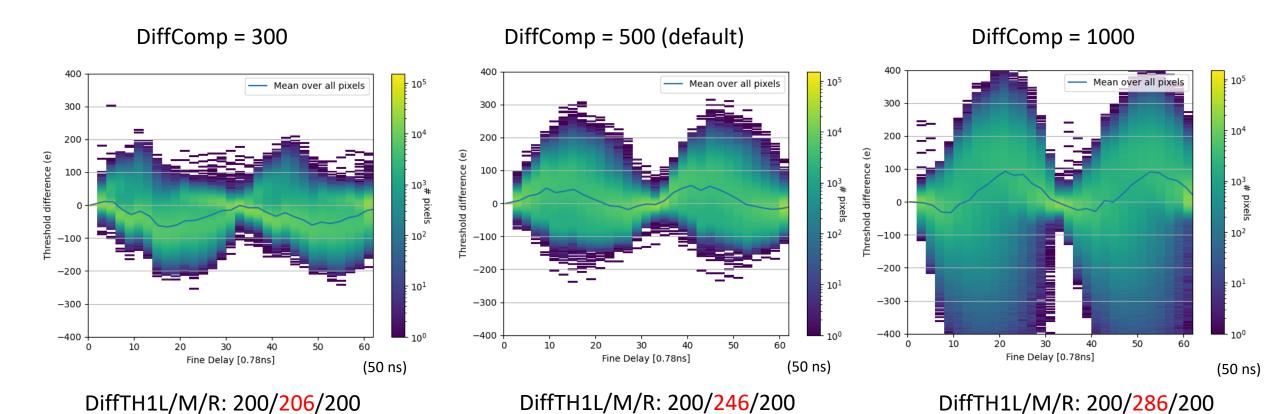


Context

- ITkPix v1.0 SCC's
- No sensor
- Single and double isolation
- Kept in freezer at -20 C

v1.0, no sensor, single iso

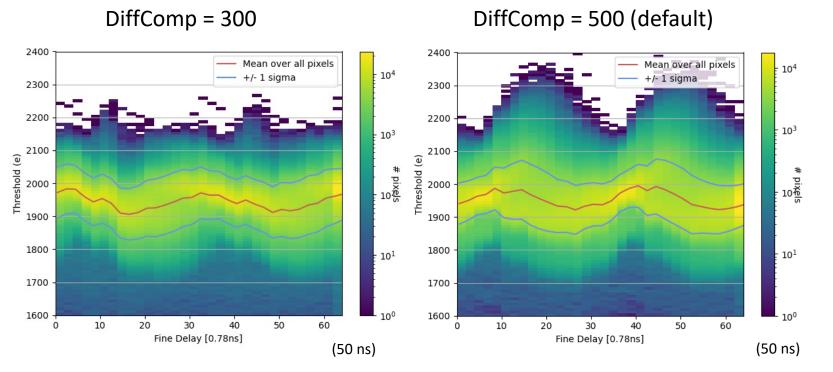
- Chip is retuned to 2000e after changing DiffComp
- Scanning fine delay (0.78125 ns each, but in steps of 2) with calledge delay = 0

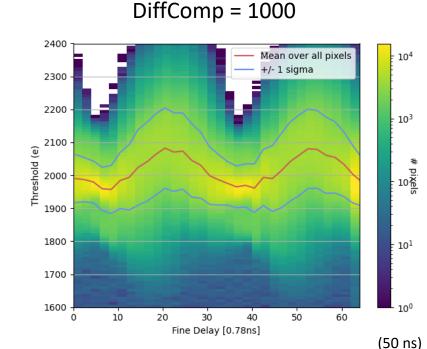


- We see 40 MHz oscillation
- Amplitude changes slightly, dispersion increases with higher DiffComp

v1.0, no sensor, single iso

- Chip is retuned to 2000e after changing DiffComp
- Scanning fine delay (0.78125 ns each, but in steps of 2) with calledge delay = 0





DiffTH1L/M/R: 200/206/200

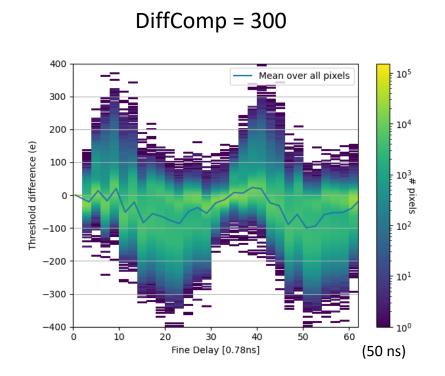
DiffTH1L/M/R: 200/246/200

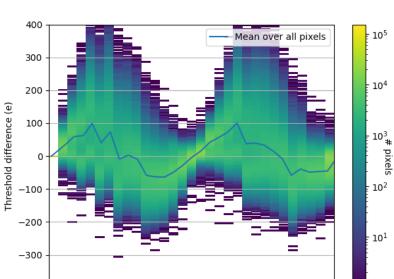
DiffTH1L/M/R: 200/286/200

- We see 40 MHz oscillation
- Amplitude changes slightly, dispersion increases with higher DiffComp

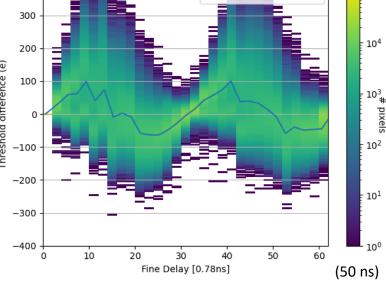
v1.0, no sensor, double iso

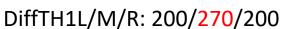
- Chip is retuned to 2000e after changing DiffComp
- Scanning fine delay (0.78 ns each, but in steps of 2) with call edge delay = 0



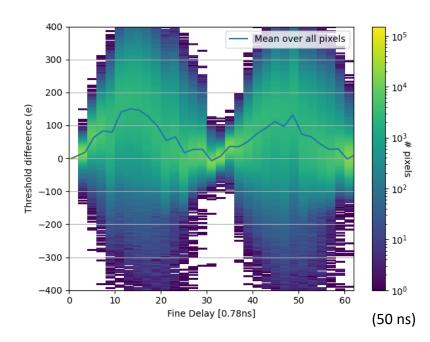


DiffComp = 500 (default)









DiffTH1L/M/R: 200/302/200

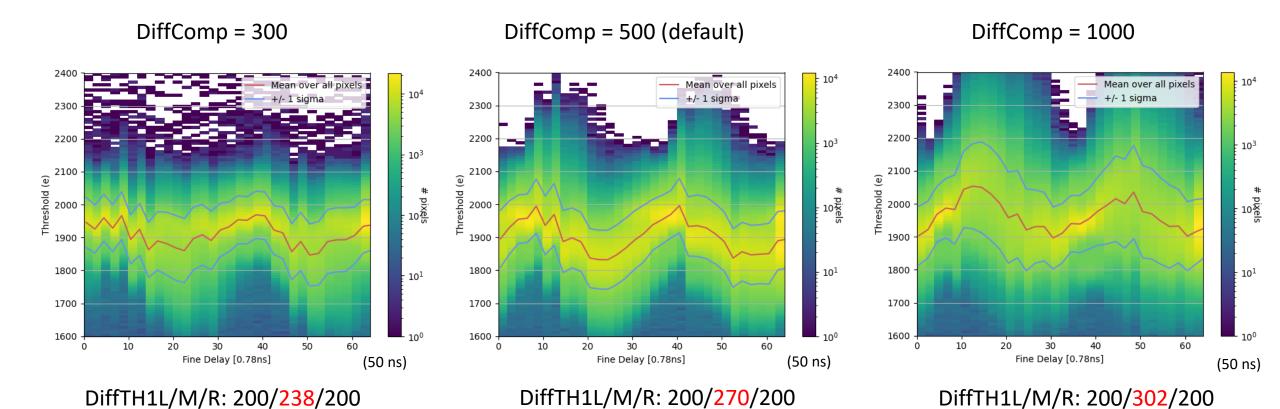
We see 40 MHz oscillation

DiffTH1L/M/R: 200/238/200

Amplitude changes slightly, dispersion increases with higher DiffComp

v1.0, no sensor, double iso

- Chip is retuned to 2000e after changing DiffComp
- Scanning fine delay (0.78 ns each, but in steps of 2) with calledge delay = 0

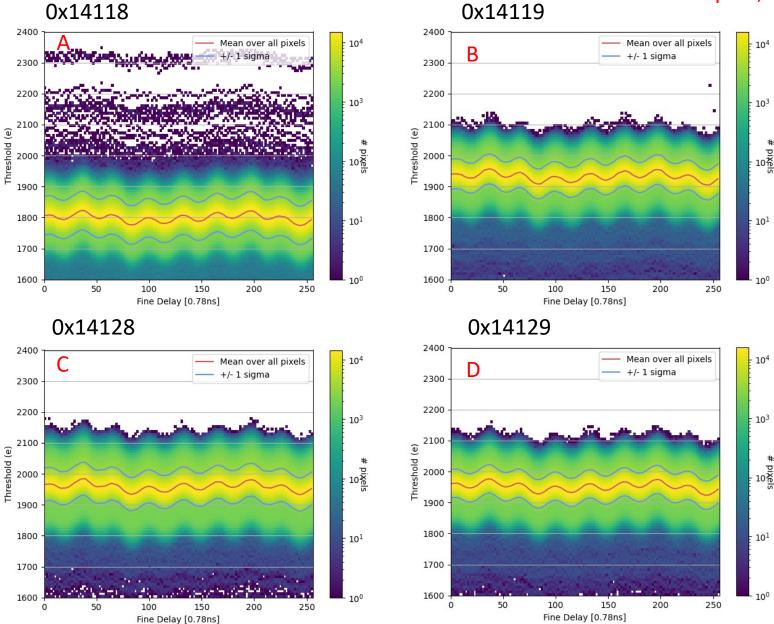


- We see 40 MHz oscillation
- Amplitude changes slightly, dispersion increases with higher DiffComp

- ITkPix v1.1 quad module
- No sensor
- Room temperature

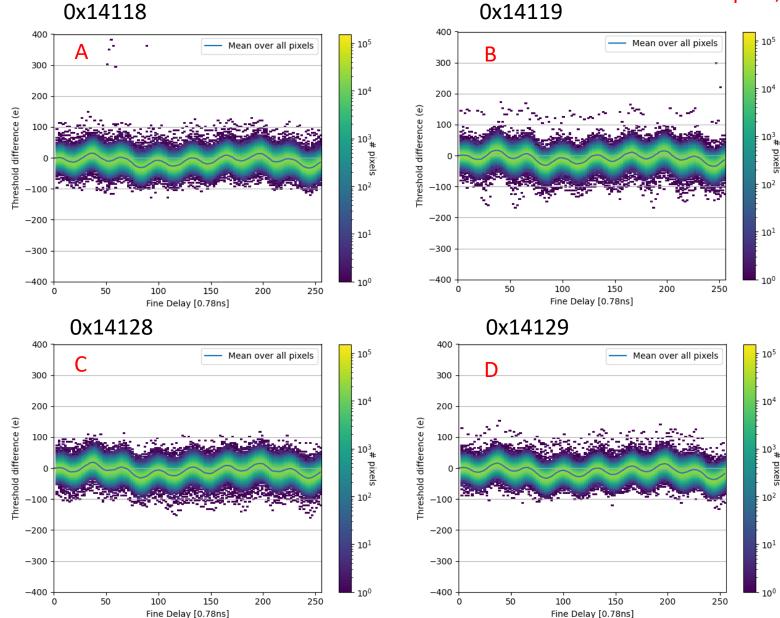
v1.1 quad, no sensor

 No sensor → 40 MHz dominant



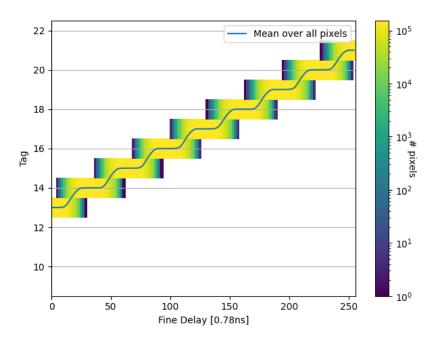
v1.1 quad, no sensor

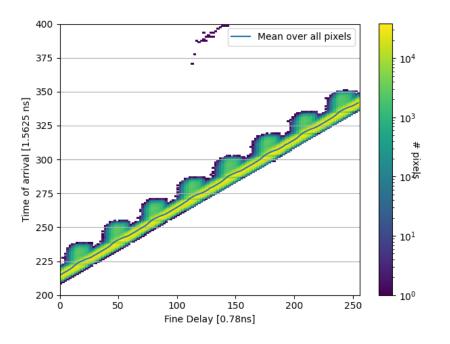
 No sensor → 40 MHz dominant



v1.1, with unbiased 3D sensor

This slide is a sanity check that we are really scanning the full 200 ns of delay



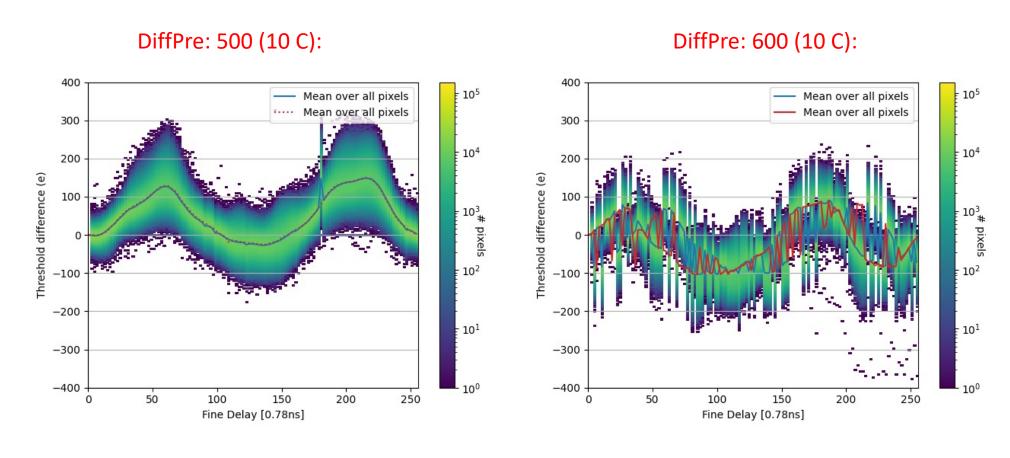


Backup: alternative plot style

The following slides show the exact same results already presented, however in a different plotting style (showing Δ thr of each pixel with respect to average)

v1.1, with unbiased 3D sensor

How stable are the threshold results? Do we see large variations when we run the exact same scan multiple times?



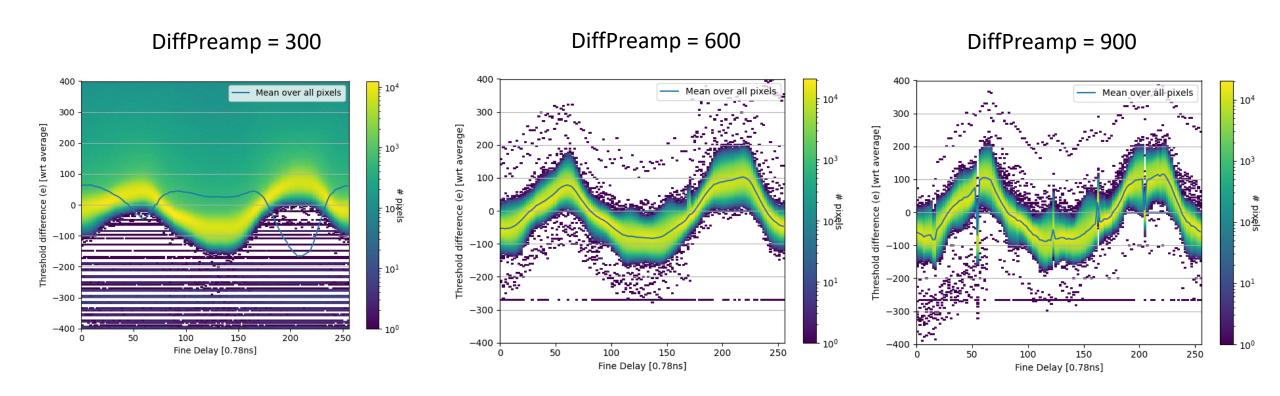
Results are very stable (w/in a few electrons)

Fluctuations seem to be random

- 1. v1.1 SCC with unbiased 3D sensor
 - 10 MHz dominant
 - No DiffPreamp dependence (though perhaps retuning is needed)
 - No temperature dependence
 - No change with LCC enabled

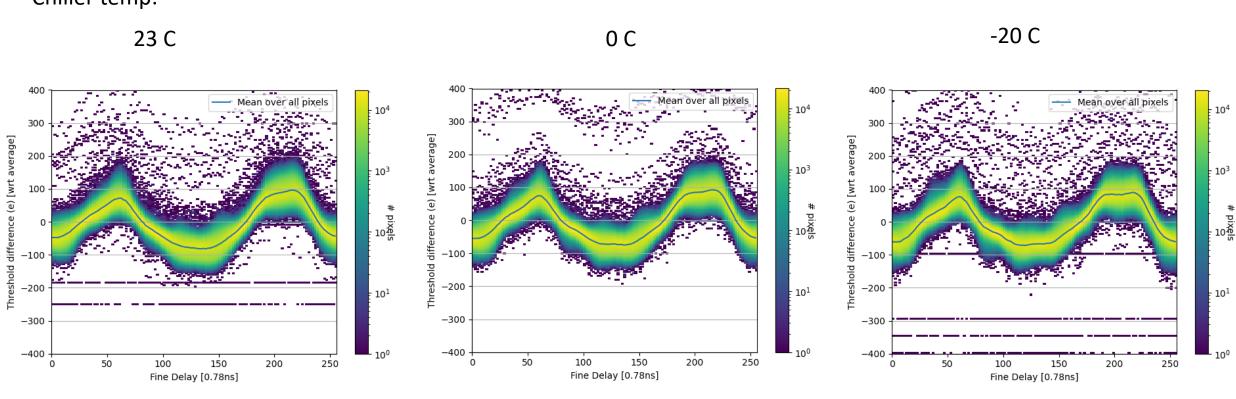
(room temperature)

No re-tuning in between DiffPreamp scans

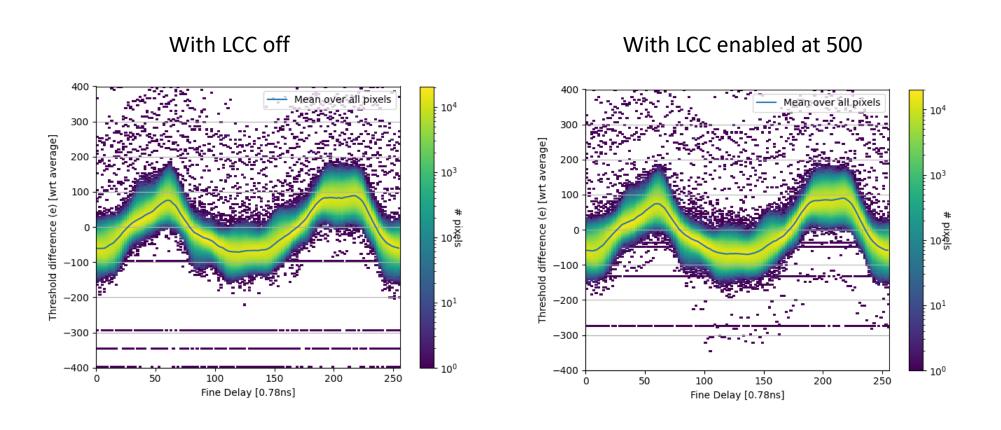


Note that there were many failed fits with this run! Because threshold increased with smaller DiffPreamp, and I wasn't capturing full s-curve. 2500 at delay = 0, and 20,000 at delay=214

Chiller temp:



Temperature: -20C



Temperature dependence

- ITkPix v1.1 quad module
- HPK planar sensor, biased @ 100V
- 15 C

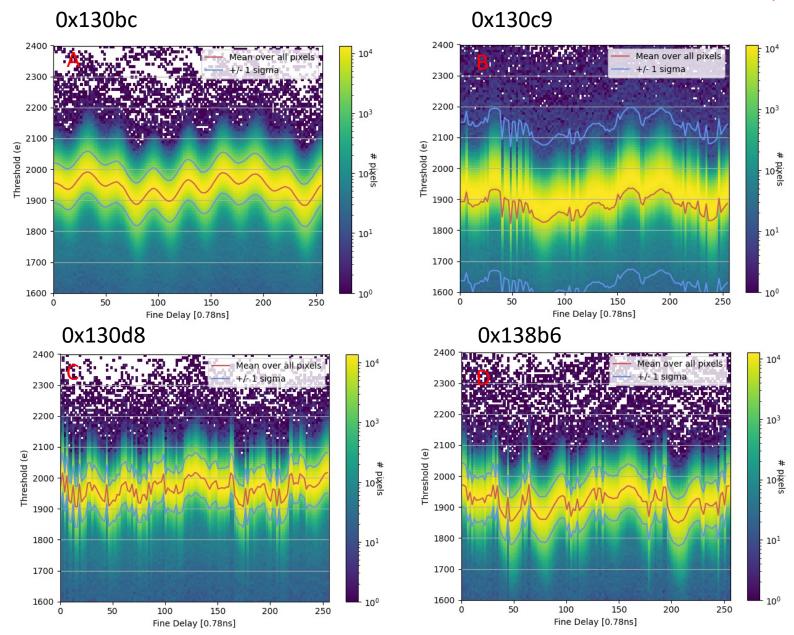
v1.1, with biased HPK planar sensor

For all chips:

DiffPreComp: 350

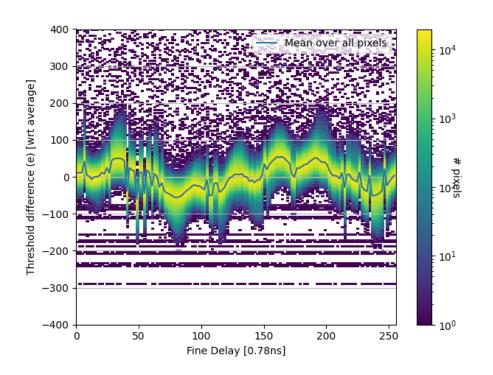
DiffPreamp: 500

DiffComp: 500

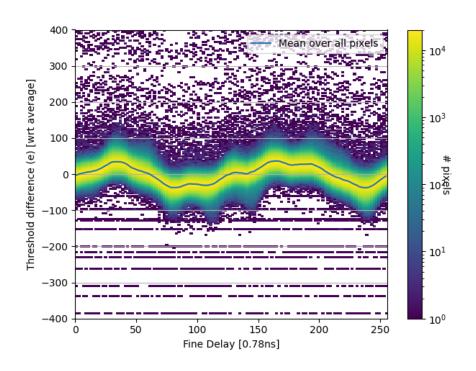


0x130bc (chip A)

DiffPreamp: 500



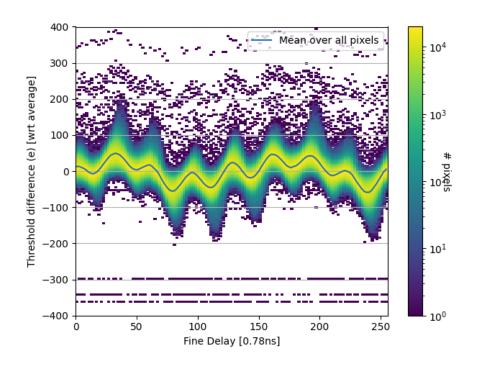
DiffPreamp: 300



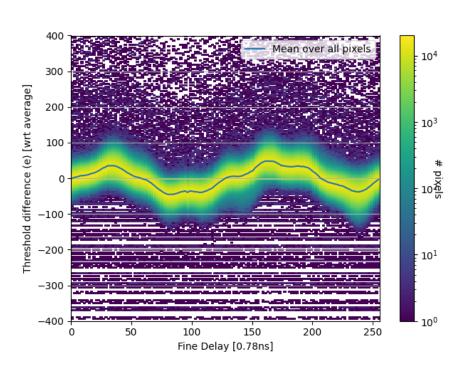
(with retuning to 2000 e)

0x130c9 (chip B)

DiffPreamp: 500



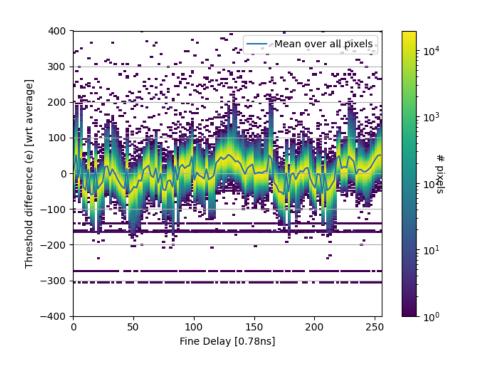
DiffPreamp: 300



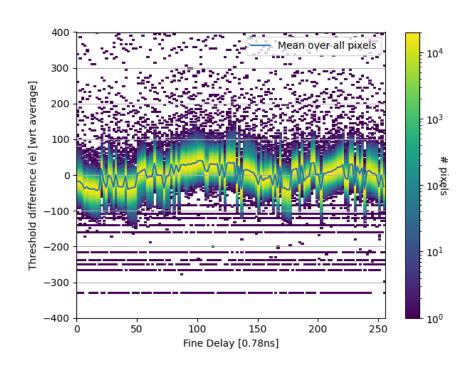
(with retuning to 2000 e)

0x130d8 (chip C)

DiffPreamp: 500



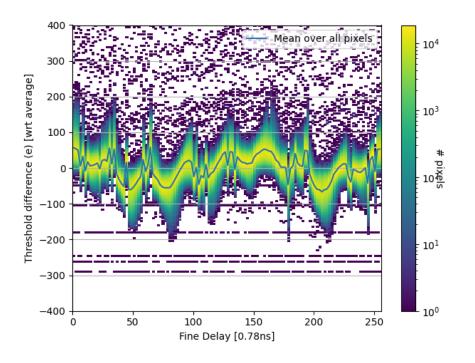
DiffPreamp: 300



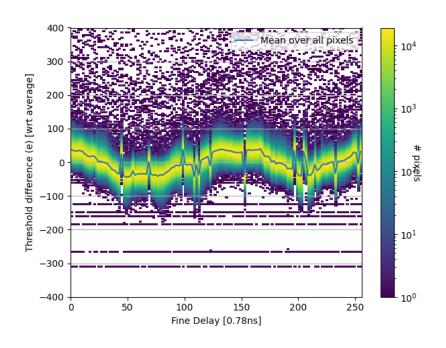
(with retuning to 2000 e)

0x138b6 (chip D)

DiffPreamp: 500

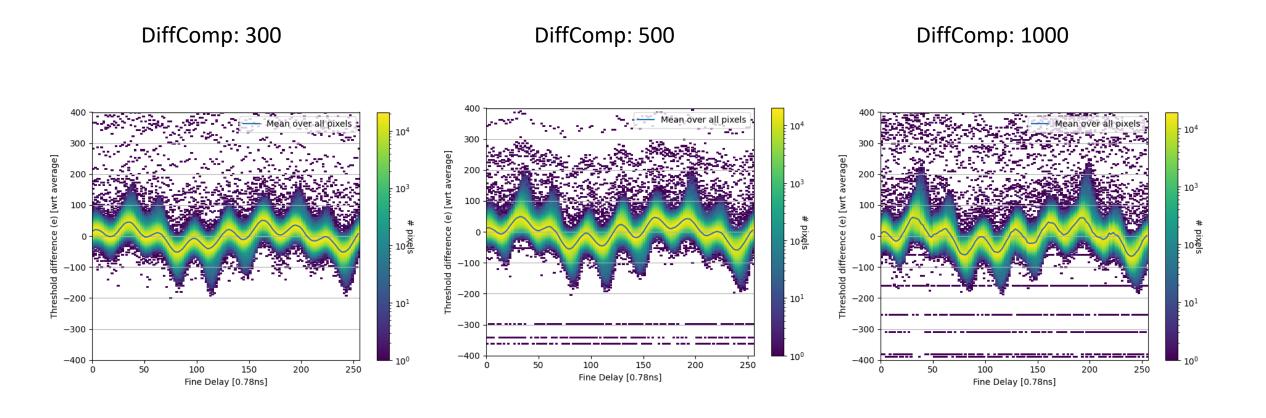


DiffPreamp: 300



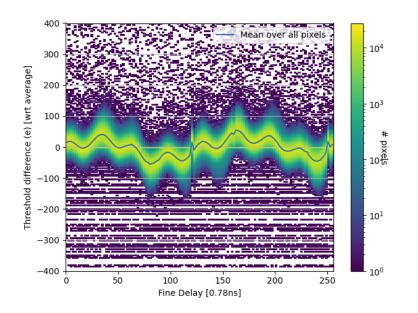
(with retuning to 2000 e)

0x130bc (chip A)

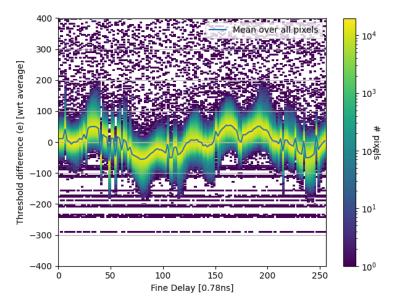


0x130c9 (chip B)

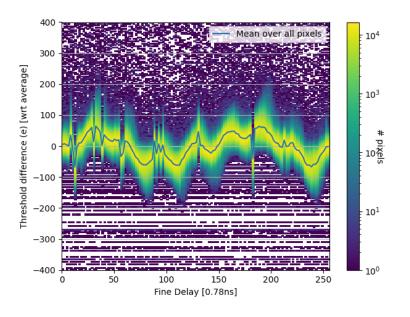




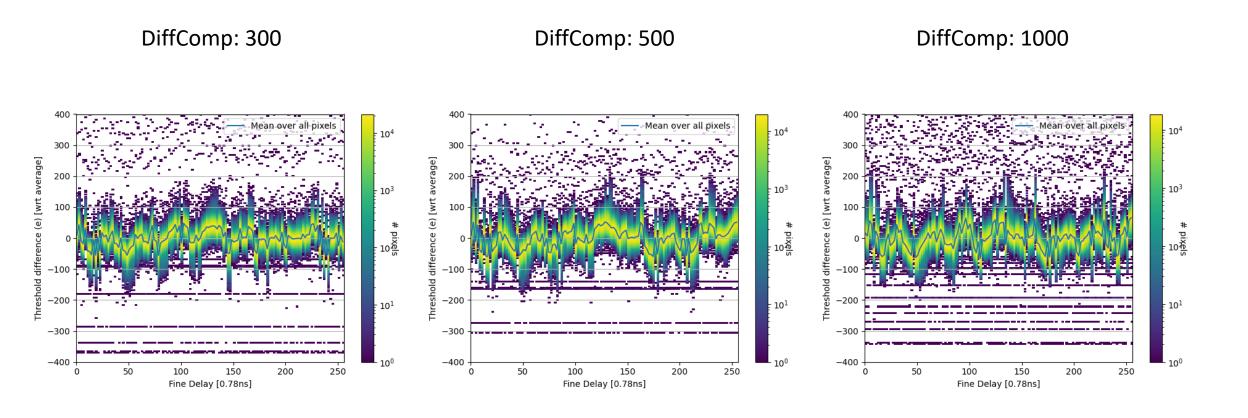
DiffComp: 500



DiffComp: 1000



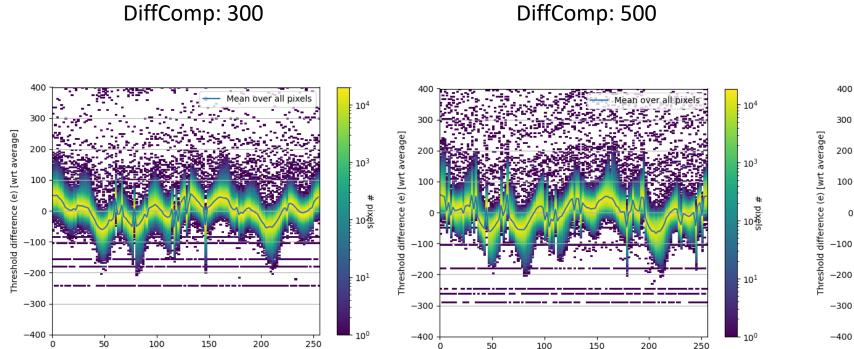
0x130d8 (chip C)



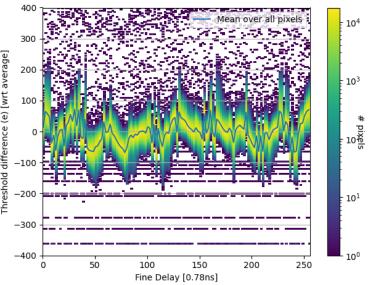
50

Fine Delay [0.78ns]

0x138b6 (chip D)



DiffComp: 1000



150

Fine Delay [0.78ns]

200

250

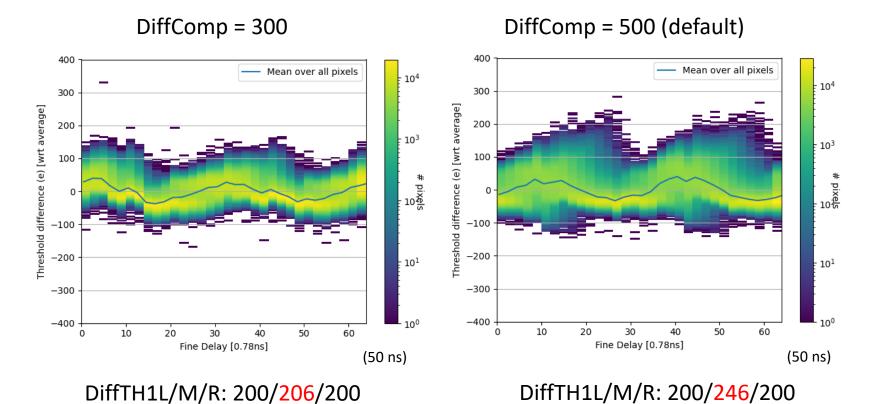
DiffComp: 500

Context

- ITkPix v1.0 SCC's
- No sensor
- Single and double isolation
- Kept in freezer at -20 C

v1.0, no sensor, single iso

- Chip is retuned to 2000e after changing DiffComp
- Scanning fine delay (0.78125 ns each, but in steps of 2) with calledge delay = 0



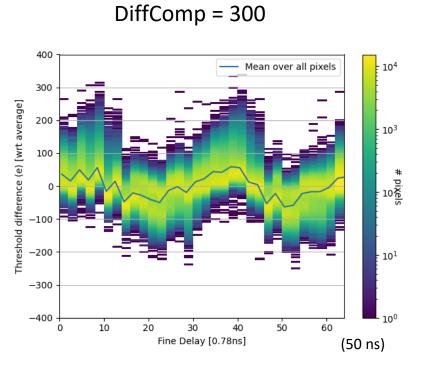
DiffTH1L/M/R: 200/286/200

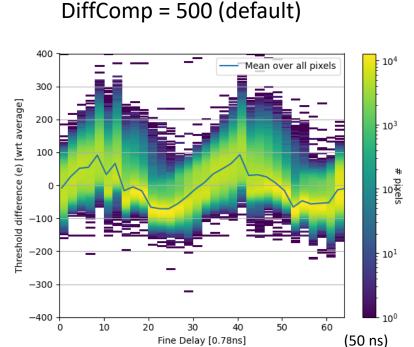
- We see 40 MHz oscillation
- Amplitude changes slightly, dispersion increases with higher DiffComp

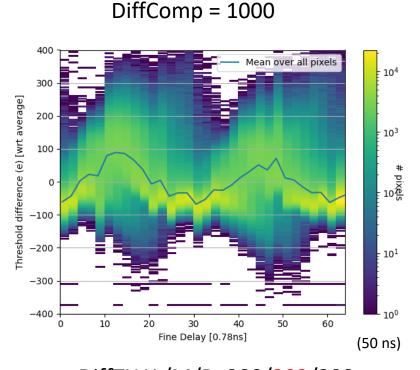
(Note x-axis range difference)

v1.0, no sensor, double iso

- Chip is retuned to 2000e after changing DiffComp
- Scanning fine delay (0.78 ns each, but in steps of 2) with call edge delay = 0







DiffTH1L/M/R: 200/238/200

DiffTH1L/M/R: 200/270/200

Fine Delay [0.78ns]

DiffTH1L/M/R: 200/302/200

- We see 40 MHz oscillation
- Amplitude changes slightly, dispersion increases with higher DiffComp

(Note x-axis range difference)

- ITkPix v1.1 quad module
- No sensor
- Room temperature

v1.1 quad, no sensor

 No sensor → 40 MHz dominant

